

Article

Impact of Etching Time on The Nanostructuring of Silicon Wafer Surfaces Using Sun Light Photochemical Method

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Abstract: Nanostructured porous silicon (PS) has emerged as a promising material due to its unique electronic and optical properties, which are influenced by fabrication techniques and etching parameters. Photochemical etching using sunlight as a natural energy source (S.L.PCE) offers a cost-effective, environmentally friendly alternative to industrial light sources. While prior studies explored porous silicon formation using artificial illumination, limited research has focused on the effects of etching time under concentrated sunlight. This study investigates the impact of varying etching durations (40–70 minutes) on the morphology and structural properties of PS layers formed on (111) N-type silicon wafers. Using a convex lens to focus sunlight and a fixed 40% HF concentration, etching was performed, and atomic force microscopy was employed to assess topography, thickness, and particle distribution. The results demonstrate that increased etching time leads to thinner wall structures, smaller nanoparticle diameters (101.18 nm to 66.36 nm), and thicker porous layers (9.79 nm to 51.38 nm), accompanied by an increase in surface roughness and RMS values. Statistical analysis showed negatively skewed and high-kurtosis particle size distributions, indicating non-normal distribution shapes with sharper peaks. The study is among the first to systematically evaluate the influence of solar-based etching duration on PS layer characteristics using real sunlight, avoiding artificial heating and vapor hazards. Findings offer insights for optimizing low-cost, green fabrication of nanoscale silicon for potential applications in optoelectronics, photodetectors, and sensing technologies.

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1. Introduction

A silicon nanocrystal, a small piece of Si, contains a few tens to a little ten thousands of atoms with archetypal dimensions of one to ten nanometers[1]. Regions of silicon in nano meter size surrounded by emptiness space forming as a network [2]. Porous silicon is combination between silicon and void space of nanoporous that create a micro structure and provide a large surface to volume ratio[3]. The discovery of the importance of silicon in the zero dimension was in the 1990[4]. With Canham's discovery of the phenomenon of photo luminescent (PL) in porous silicon, which was explained on the basis of the phenomenon of quantum confinement of charge carriers. "quantum confinement" means considering the approximation of electrons and holes effective mass in semiconductors and the "particle in a box" problem [5]. Solving the non-timedependent Schrodinger equation gives us the energy levels separation with inversely proportional between the energies and the square of the potential well width, (when the potential well width is small or particles in nanosize means higher energy levels). Like this approximation, means negative effective mass for the holes, which leads to an increase in the energy band gap[6].

Photoluminescence in the visible area (400-800 nm or 1.5-3 eV), which occurs after exciting the samples with a blue light source (High energy) is often considered evidence of quantum confinement. Because photoluminescence in bulk silicon is in the infrared region (1.1 eV). Quantum confinement not only expands the energy gap and thus allows for luminescence in the visible region in Si, but also enhances the efficiency of this luminescence. The bandgap in Bulk silicon is indirect, that means in order to moderate the electron - hole recombination, a phonon is needed so as to conserve crystal momentum. at room temperature, this is an inefficient process (<0.001%). Confining carriers in real space, causing to spread their wave functions out in k-space and the band gap is became more direct-like [7]. Electronic and optical properties desirable for various applications in optoelectronics [8] energy storage [9] biomedicine [10] Photonic crystal [11], Gas sensor [12] solar cells [13] photodetector [14,15,2].

Silicon nanocrystals can be fabricated by various methods such as Photoelectrochemical etching [16,17] Laser- induced etching [18,19] Pulsed laser deposition (PLD) [20] Femtosecond-laser pulses technique [21] laser-assisted electrochemical etching [22] photochemical etching using a halogen lamp [23] electrochemical etching [3] [4] [13] [24] [25] Aerosol techniques [1] green synthesis [26] ordinary light-assisted etching (OLAE) [27] laser-assisted etching (LAE) [14] [15] [27] Stain etching [28]

The most acceptable dissolution mechanics was presented by Lehmann and Gösele, It is based on a surface bound oxidization scheme, with hole capture, and subsequent electron injection, which leads to the divalent Si oxidization state. The injected holes attack the bond between silicon and hydrogen, breaking it and being replaced by fluorine ions. The second attack of fluoride ions leads to the of evaporation hydrogen, which results in electron injection to the substrate. The back bonds between silicon and silicon are broken due to HF attack. The remaining surface silicon atoms will combine with hydrogen atoms, resulting in a silicon tetrafluoride molecule, which reacts with two molecules of HF forming H_2SiF_4 which later ionizes to SiF_6^{2-} and 2H^+ [29].

The etching processing of Silicon in HF produce surfaces with different properties because of the various rates for etching processing that improve on the tips and on the pores walls. At the tips, because of the highest curvature radius, the carriers are preferentially collected which is responsible for a large electric field. For densities more than a critical carrier density, The holes that reach the tips cannot be consumed all for a long time, the process of dissolution begins also on the walls of the pores, the electropolishing process takes place, this process is different for n-type silicon than for P-type silicon. Figure (1). In n-type silicon, the substrate illumination leads to the photogeneration of holes that move towards the surface, where they spread at the pits of the pores and enter the solution. The spread of these holes is responsible for limiting the formation of macropores. While permanent availability for holes in p-type silicon, it is sufficient to generate current during the etching process to form macro pores. very different structures were produce from these two different mechanisms. In n-type silicon, the carrier concentration within the porous regions can be precisely regulated through the intensity of incident light. This control allows the formation of pores with high aspect ratios, as lateral expansion is effectively suppressed. On the other hand, in p-type silicon substrates, although the lateral growth of pores can be minimized, it cannot be completely eliminated. The spatial distribution of macropores also varies depending on the surface treatment. When no pre-patterning is applied, macropores tend to appear in a random arrangement across the surface. However, when the silicon surface is patterned with etch-pits using photolithography followed by potassium hydroxide (KOH) etching, the macropores form in a well-ordered, regular pattern [30] (Figure 1).

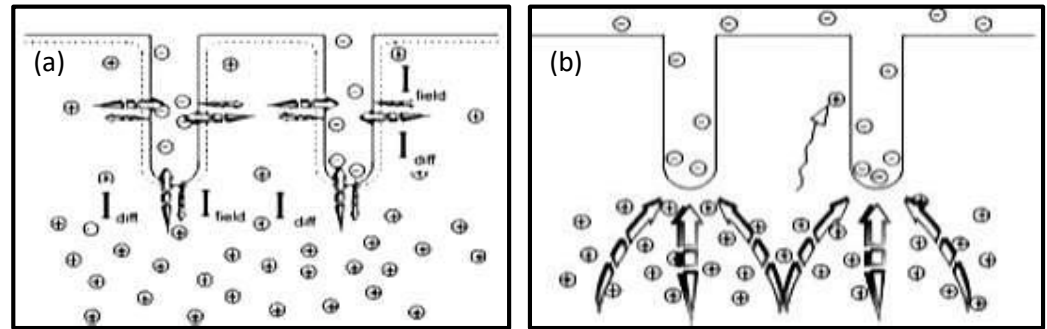


Figure (1): (a) showed how the dissolution process in p-type silicon causes expansion of pores during etching due to lateral currents during the etching process. (b) schematic diagram of the dissolution in n-type silicon, in which completely depletion of carriers in semiconductor between the pores [30].

The use of sunlight as a substitute for industrial sources (S.L.PEC) method was applied practically for the first time in 2018 where lenses of different diameters used to obtain different light intensities sufficient and appropriate to form layers of porous silicon with nanoscale dimensions that were studied using atomic force microscopy, as well as studying the electrical properties of diodes resulting from the formation of a P-N junction between the Bulk layer and porous layer obtained from photochemical etching processing. The results of the study of the electrical properties (I-V) showed different rectification properties depending on the preparation conditions [31].

There are several factors that affect the shapes and sizes of particles that formed in the porous layer and the thickness of this layer, the proportion among these variables will produce layers with different properties these factors and variables have been presented in many research and studies: type of crystalline silicon, light power density[31], silicon resistivity etching time[23] HF acid concentration[26,32]

2. The Practical Part

N-type crystalline silicon with resistivity of (1-10 Ω .cm) and (111) orientation were cut in dimensions (0.5cmx0.5cm), after cutting, they were cleaned using distilled water and ethanol. In order to remove the oxide layer formed on the surfaces of the samples, they are immersed for 10 minutes in 40% HF acid.

The system used in this study consists of a Teflon container with a diameter 7 cm, internal height 3 cm, external height 4 cm Fig.3 (a), designed in its inner center a U-shaped stand of the same material Fig.3 (c), In order the silicone samples to settle on them, which were cut to dimensions suitable with the dimensions of the backing, the Teflon container is filled with hydrofluoric acid so that it covers the upper surface of the sample by several millimeters and is placed under the sunlight collected by the lens. We used a lens with a diameter of (9 cm) and a focal length of (30 cm), the angle of inclination was adjusted Mounted on a stand that allows free movement of the lens so that the focus of the sunlight spot is ensured on the shiny side of the surface of the (mirror-like) silicon samples Fig.3 (b, c). It is possible to calculate the light power density on the surface of the lens, as the average value of the radiation intensity reaches on each square meter on earth (Solar Flux Density Reaching Earth) is about 1367 w/m²[33,34]. In this study, two methods were used to calculate the light power density incident on the samples surface: first by using a Digital Light Meter (Lutron LX-103), second using the arithmetic method, assuming that the realistic average irradiance value of 1000 W/m² (equivalent to 100 mW/cm²) was adopted as the standard working condition. The total incident power was estimated by multiplying the lens area by the solar irradiance per square centimeter, resulting in a value of 6360 mW. When this amount of light energy is concentrated onto a 1 cm² sample surface, the resulting power density is 6360 mW/cm², or 6.36 W/cm². If the same light is instead focused onto a 0.25 cm² area, the intensity increases by a factor of four, reaching 25.44 W/cm². This level of energy is sufficient to drive the etching process. The experimental setup in this work achieved this same energy level, as presented in Table 1.

Table (1) Shows the lens parameters and the calculation of the luminous intensity on the surface of the samples

Lens radius (r) (cm)	lens area (cm ²) $A=\pi r^2$	Optical power over the entire surface of the lens $P(mw)$	light intensity focused on the sample area 1 cm ² $I(mw/cm^2)$	light intensity focused on the sample area 0.25 cm ²
4.5	63.6	$100 \frac{mw}{cm^2} \times 63.6 cm^2$ =6360 mw	$6360 \frac{mw}{cm^2}$ Or $(63.6) \frac{w}{cm^2}$	$6.36 \frac{w}{cm^2}$ $\frac{0.25 cm^2}{25.44 w}$

Figure 2 illustrates the experimental setup for Sunlight Photochemical Etching (S.L.PCE) of silicon wafers. It includes (a) the chemical reagents and lens system, (b) the illuminated Teflon container under focused sunlight, and (c) a schematic showing the lens, sample holder, and HF acid arrangement for nanoscale surface etching [19].

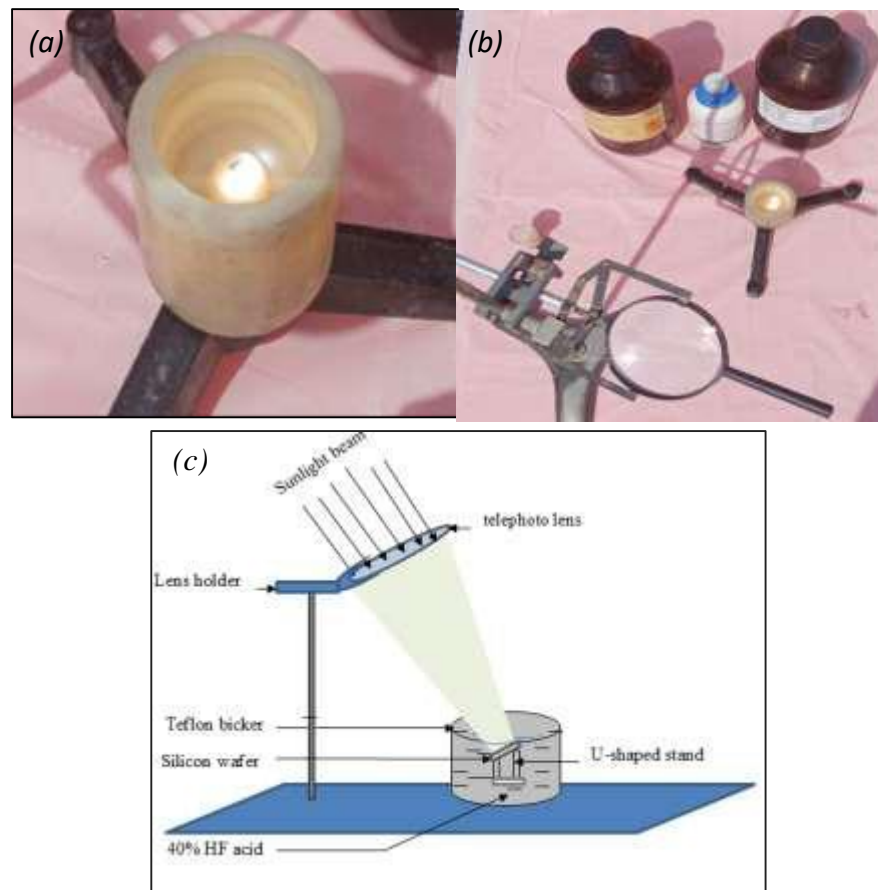


Figure (2): (a) The Teflon container (b) showing the setup of the Sun light photochemical etching (S.L.PCE) system (c) a schematic diagram of the experimental set-up.

It is considered high energy and is available at lower costs compared to other light sources that are accompanied by some physical and health damages, the associated heat when using industrial sources such as halogen and tungsten leads to rapid evaporation of the acid used, which requires compensating for the shortage of acid continuously, in addition to the health damages resulting from inhalation of acid vapor [20]. While these cases do not occur in this method that has been adopted (photochemical etching using

sunlight). The appearance of some bubbles on the surface of the sample during the experiment is evidence of the beginning of the chemical dissolution process, which are hydrogen bubbles resulting from the chemical dissolution reactions of silicon[31].After a short period, the surface color also changed to a buffish brown color (the color of iron rust) another evidence of the formation of the porous layer, This process continues along the etching time, which is fixed for all samples (60min). after completing the Preparation process , the samples are removed from the Teflon container and placed in plastic containers filled with methanol in order to preserve them from oxidation .

3. Results and Discussion

Atomic force microscopy results showed the formation of nano sized porous silicon layers whose thickness increased from 9.79 nm to 51.38 nm Figure(3) and their surface roughness increased from 2.28 nm to 9.13 nm the Root Mean Square (RMS) roughness changed from 2.5nm to 11nm with increasing photochemical etching time from 40 min. to 70 min respectively .

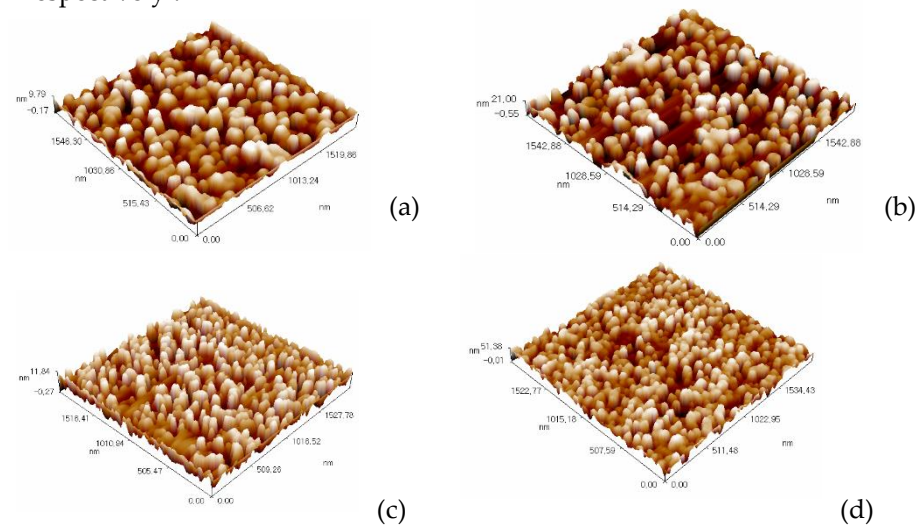


Figure (3): AFM image of the porous layer in silicon prepared at different etching time of (a) 40, (b) 50, (c) 60, and (d) 70 min.

These changes in the (RMS), roughness and thickness of the porous layer can be explained by the mechanism through which the holes contribute to the etching process. In N-type silicon, the holes are photo generated, depending on the light intensity falling on the surface as well as the doping density of the substrate . These holes move inside the sample towards the surface to compensate for those that were consumed in the dissolution processes (hole injection and attack on a Silicon –Hydrogen bond by a fluoride ion). In any case, the current density of the holes must be less than the critical factor to avoid the occurrence of the electroplating process[35]. The holes that reach the pore bottom curve contribute more to vertical dissolution, figure (1- b)[30], as the etching time for this process increases, the thickness and roughness of the porous layer increases, Table (2) and Figure (4).

Table (2): Shows information about porous silicon layers

Sample Etching time Min	root mean square (nm)	Layer thickness (nm)	Average of surface roughness (nm)	Avg. Diameter (nm)
40	2.65	9.79	2.28	101.18
50	6.22	21	5.39	72.00
60	3.13	11.84	2.66	68.94
70	11	51.38	9.13	66.36

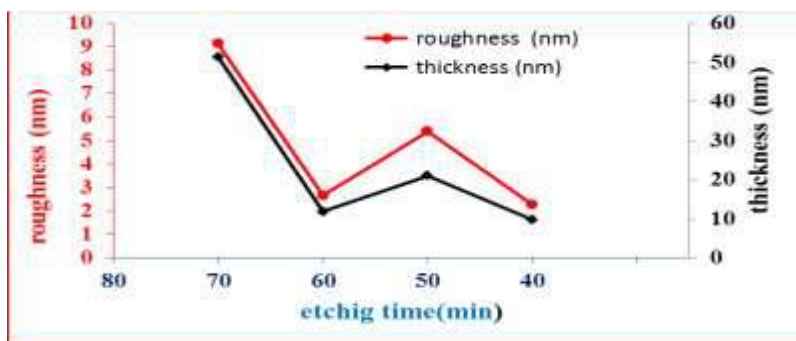


Figure (4). The thickness and roughness of the samples at each etching time

While the holes that reach the top of the sample surface contribute more to horizontal dissolution (Side etching), which leads to a reduction in the thickness of the walls between the pores and their partial erosion, forming particles with different diameters, sizes and shapes within the nanoscale dimension. Their size decreases with increasing etching time. This explains the change in the particles diameter formed on the surfaces from 101.18 nm to 66.36 nm with a change in time. Etching from 40 minutes to 70 minutes Figure (5).

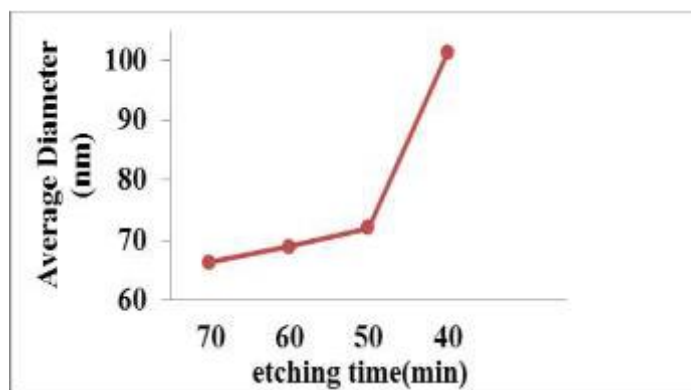


Figure (5): average diameters of nanoparticles at each etching time.

Figure (6) provides an explanation of the statistical distribution of particles formed on the surfaces of porous silicon layers, the percentage of diameters and sizes of these particles, as well as the cumulative percentage, and information about skewness and kurtosis figure (6- a, c, d) shows the statistical distributions of the particles formed on the samples prepared under an etching time of 40, 60, and 70 minutes with negative skewness values (-0.117, -0.185, -0.434) (skewness is a measure of the asymmetry of the probability distribution of a real-valued random variable about its mean)[36], for average diameter of the particles (101.18, 68.94, 66.36) nm respectively. While the sample that was prepared at an etching time of 50 minutes, the statistical studies showed a small positive skewness value (0.000471), with a probability distribution close to the normal distribution, (a skewness value of normal distribution equal to zero) Figure (6 - b) table (3).

Table (3): Amplitude parameters for the produced layers

Etching time min	Skewness of surface	kurtosis of surface	average distance between a peak to peak (nm)	ten point height (nm)
40	-0.117	1.89	9.96	5.39
50	0.000471	1.8	21.5	21.5
60	-0.185	2	12.1	6.72
70	-0.434	2.51	51.4	51.3

Figure 6 presents histograms and cumulative distributions of nanoparticle diameters on porous silicon surfaces etched for 40, 50, 60, and 70 minutes. The data reveal a decreasing trend in average particle size with longer etching durations, indicating finer nanostructure formation and enhanced uniformity as etching progresses under constant photochemical conditions [30] [31].

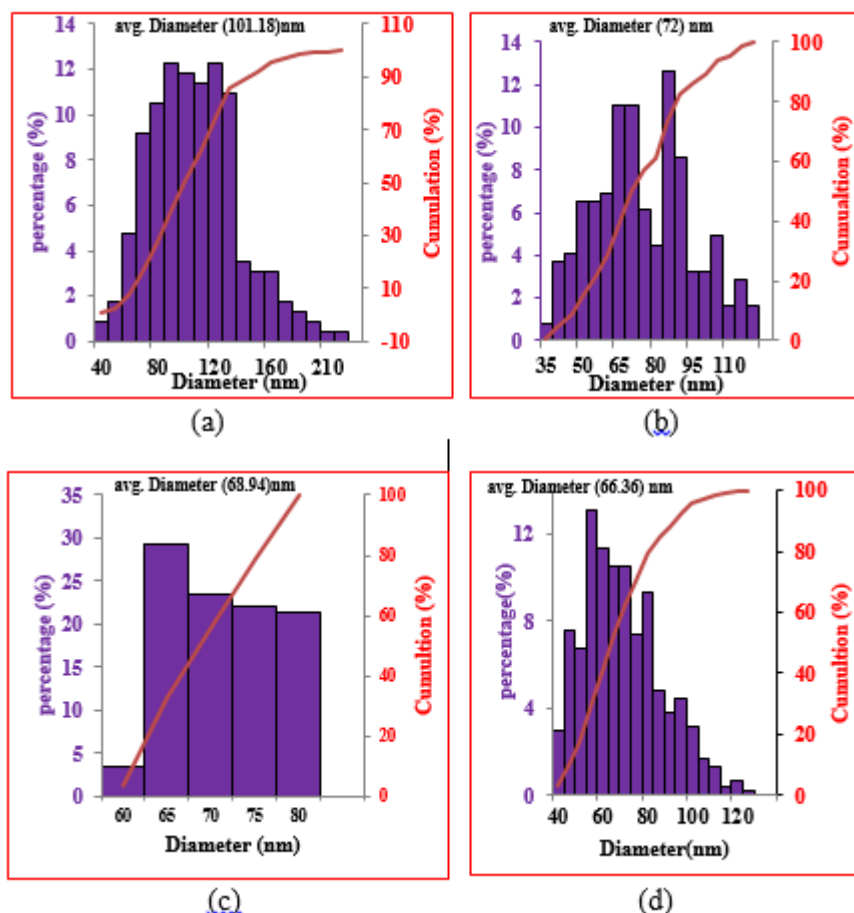


Figure (6):The statistical distribution of nanoparticle sizes on the surface of the porous silicon layer prepared at different etching time : (a) 40, (b) 50, (c) 60, and (d) 70min

Another important statistical measurement provided by the atomic force microscope regarding the distribution of nanoparticles on surfaces is kurtosis «kurtosis is a statistical number that tells us if a distribution is taller or shorter than a normal distribution . If a distribution is similar to the normal distribution, the Kurtosis value is 0. If Kurtosis is greater than 0, then it has a higher peak compared to the normal distribution. If Kurtosis is less than 0, then it is flatter than a normal distribution » [36]. Table (3) shows values of kurtosis higher than zero for all samples, which means that the statistical distributions of nanoparticles formed on the surfaces of the samples are higher than the normal distribution, (kurtosis of normal distribution is zero). Figure (7) shows the statistical measurements of both the skewness and kurtosis of the distribution of nanoparticles formed on the surfaces of porous silicon samples as a function of the etching time.

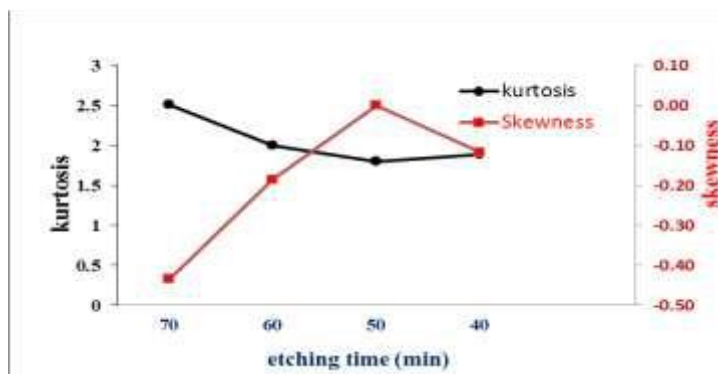


Figure (7). surface kurtosis (black line) and skewness (red line) for different etching time.

4. Conclusion

The shape and size of nanoparticles formed on the surfaces of samples etched by the photochemical method focusing of sunlight is a function of several parameters during the preparation process, including: HF acid concentration, density of substrate doping, etching time, type of silicone, intensity of the light and the distribution of this light density on the surface. Increasing the etching time led to the creation of smaller particles, and an increase in (RMS), thickness and roughness of this layers as a result of two types of dissolution to which the photogenerated holes contribute: 1) the dissolution processes which are close to the surface towards the sides of the pores (horizontal etching) lead to reducing the thickness of the walls between the pores and partially dissolving them, thus forming particles with smaller diameters. 2) Dissolution processes towards depth, which occur at the bottom and sides of the pores, they lead to an increase in the thickness of the porous layer. Both of the above processes increase with increasing etching time. Light passing through a converging lens undergoes a Gaussian distribution, which means that the etching processes are greater at the center of the beam than at its edges. So the particles formed in the center of the sample are smaller and their diameters increase as we approach the edge of the sample due to incident light distribution. Therefore, the sizes and diameters of the particles formed on the surface are close to the same distribution above.

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